



PATENT

Docket No. IL-10092

Assistant Commissioner of Patents  
Washington, DC 20231

**NEW APPLICATION TRANSMITTAL**

Transmitted herewith for filing is the patent application of Inventor(s):

Paul Wickboldt/Paul G. Carey/Patrick M. Smith/Albert R. Ellingboe

For (title): Deposition Of Dopant Impurities And Pulsed Energy Drive-In

**1. Type of Application**

☒ This new application is for an original patent.

☐ This new application is a:

- ☐ Division
- ☐ Continuation
- ☐ Continuation-in-part (CIP)

**2. Benefit of Prior U.S. Application(s) (35 USC 120)**

☐ The new application being transmitted claims the benefit of prior U.S. application(s).

**3. Papers enclosed which are required for filing Date Under 37 CFR 1.53(b).**

10 Pages of specification

4 Pages of claims

1 Pages of abstract

1 Sheets of drawings

- ☒ formal
- ☐ informal

**4. Additional papers enclosed**

- ☐ Preliminary Amendment
- ☐ Information Disclosure Statement
- ☐ Form PTO-1449
- ☐ Declaration of Biological Deposit
- ☐ Special Comments
- ☐ Other

5. Declaration or oath

- ☒ Enclosed and executed by
- ☒ inventors
  - ☐ legal representative of inventor(s) 37 CFR 1.42 or 1.43
- ☐ Not Enclosed

6. Assignment

- ☒ An assignment of the invention to The Regents of the University of California.
- ☒ is attached
  - ☐ will follow

7. Certified Copy

Certified copy(ies) of application(s)

(country)	(application no.)	(filed)
(country)	(application no.)	(filed)

from which priority is claimed

- ☐ is(are) attached.
- ☐ will follow

8. Fee Calculation

CLAIMS AS FILED					
Type of Claim	Number Filed	Included in Basic Fee	Number Extra	Rate	Total Fee
Total Claims	20	-20 =	0	x \$22 =	\$ 00.00
Independent Claims	3	-3 =	0	x \$80 =	\$ 00.00
				Multiple Claims =	\$
				Basic Filing Fee =	\$ 770.00
				Sub-Total =	\$ 770.00
				Small Entity Filing Fee =	\$ 385.00

9. Small Entity Statement(s)

- ☒ Verified Statement that this is a filing by small entity under 37 CFR 1.9 and 1.27 is attached.

Filing Fee Calculation (50% of regular filing fee) \$ 385.00

10. Fee Payment

- ☐ Not Enclosed  
☒ Enclosed (See Account No. Below)

Total Basic Filing Fees To Be Paid \$ 385.00

11. Method of Payment of Fees

- ☐ Check in the Amount of \$ \_\_\_\_\_  
☒ Charge Account No. 12-0695 in the amount of \$ 385.00

A duplicate of this transmittal is attached.

12. Instructions As To Overpayment/Underpayment

- ☒ credit/charge  
Account No. 12-0695  
☐ refund

Reg. No.: 28,535

Tel. No.: (510) 422-7816

Dated: June 9, 1997

  
Henry P. Sartorio  
P.O. Box 808, L-703  
Livermore, CA 94551

S-86,990

RL-13,809

IL-10092

DEPOSITION OF DOPANT IMPURITIES

AND

PULSED ENERGY DRIVE-IN

BY

Paul Wickboldt (USA)  
1164 Lincoln Avenue #146  
Walnut Creek, CA 94596

Paul G. Carey (USA)  
373 Paul Avenue  
Mountain View, CA 94041

Patrick M. Smith (USA)  
555 Canyon Wood Circle #250  
San Ramon, CA 94583

Albert R. Ellingboe (USA)  
426 Washington Boulevard  
Fremont, CA 94539

DEPOSITION OF DOPANT IMPURITIES

AND

PULSED ENERGY DRIVE-IN

The United States Government has rights in this invention pursuant to Contract No. W-7405-ENG-48 between the United States Department of Energy and the University of California for the operation of Lawrence Livermore National Laboratory.

BACKGROUND OF THE INVENTION

5           The present invention relates to the incorporation of dopant impurities, particularly to depositing dopant impurities in the surface of a semiconductor, and more particularly to an enhanced doping process involving deposition of a dopant on the semiconductor surface followed by melting of the surface and drive-in of the dopant using pulsed energy to be  
10           absorbed near the surface.

          Various techniques have been developed for incorporating a dopant into a material. One of these techniques is known as Gas Immersion Laser Doping (GILD). The GILD technique involves irradiating a material, such as a semiconductor surface, with a pulsed laser in the presence of a dopant  
15           ambient (such as  $\text{BF}_3$ ,  $\text{PF}_5$ ,  $\text{AsH}_3$ , etc.). The GILD process relies on the dopant

molecules adsorbing on the semiconductor surface, the laser pulse melting a surface region of the semiconductor, the dopant being incorporated into the molten region of the semiconductor, and the molten region solidifying after the laser pulse, the dopant atoms being electrically active after solidification of the semiconductor. Using the GILD technique, the maximum dose of electrically active dopant atoms per pulse is limited to a fraction of the adsorbed dopant molecules (about  $10^{13} \text{ cm}^{-2}$ ). Useful dopant doses are above  $5 \times 10^{14} \text{ cm}^{-2}$ . Thus, the GILD technique requires at least 50 doping pulses for practical use.

The present invention involves a semiconductor doping process which enhances the dopant incorporation achieved using the GILD technique. The doping process of the present invention enhances the doping per pulse by increasing the surface concentration (and possibly thickness) of dopant molecules on the semiconductor surface prior to an energy pulse, thus resulting in a greater dose of dopant per pulse. Thus, by the two-step process of the invention involving: 1) dopant deposition onto a semiconductor surface, and 2) dopant incorporation in the semiconductor by pulsed laser or pulsed ion beam energy, the number of pulses required to achieve useful active dopant concentrations is significantly reduced compared to the GILD technique. The deposition of the dopant on the semiconductor surface can be accomplished by a number of known deposition techniques. The energy pulse may be either that of a pulsed laser or of a pulsed ion-beam source.

#### SUMMARY OF THE INVENTION

It is an object of the present invention to provide a process for the incorporation of dopant impurities in a material.

A further object of the invention is to provide a doping process which enhances manufacturability of pulsed laser-based semiconductor doping and junction formation.

5 A further object of the invention is to provide a doping process which enhances manufacturability of pulsed ion-beam semiconductor doping and junction formation.

A further object of the invention is to provide a process which enhances the dopant incorporation in a semiconductor.

10 A further object of the invention is to provide a semiconductor doping process which involves deposition of a dopant on a surface of a semiconductor and drive-in of the dopant into the semiconductor by either a pulsed-laser or a pulsed-ion-beam.

15 Another object of the invention is to provide a semiconductor doping process which enhances the dopant incorporation achievable using the Gas Immersion Laser Doping (GILD) technique.

Another object of the invention is to provide a doping process which enhances the dopant incorporation achievable using pulsed ion-beam doping techniques.

20 Another object of the invention is to provide a doping process which involves depositing a layer of dopant atoms on a surface of a material to be doped, followed by exposure to one or more energy pulses (either laser or ion-beam) which melt the surface thus causing the dopant to be incorporated into the molten region.

25 Another object of the invention is to provide a two-step doping process involving deposition of a dopant on a surface of a material, and

melting of the surface using pulsed energy (supplied by either a laser or an ion-beam), whereby the dopant is diffused into the surface of the material.

Another object of the invention is to provide a doping process utilizing pulsed laser or pulsed ion-beam processing of a dopant deposited on a semiconductor surface thereby resulting in a greater dose of dopant per energy pulse than the dopant dose produced by the Gas Immersion Laser Doping technique.

Other objects and advantages of the present invention will become apparent from the following description and accompanying drawings. The invention is basically a two-step process for incorporating electrically active dopant atoms into a material, such as a semiconductor, and involves the deposition of dopant impurities on a material surface, and pulsed laser or pulsed ion-beam drive-in of the dopant into the material surface. The deposition of the dopant can carry techniques including plasma enhanced chemical vapor deposition (PECVD), also known as glow discharge CVD, sputtering, condensation through cooling the material to be doped, thermal decomposition CVD (such as LPCVD or hot-wire CVD), and photolytic decomposition. The pulsed energy drive-in of the dopant can be carried out, for example, using a 308 nm wavelength XeCl excimer laser with a pulse duration or length of 40 ns (below 1 ms) and energy density of 600 mJ cm<sup>-2</sup>. The first half of the doping process of the present invention enhances the dopant incorporation achieved using the gas immersion laser doping (GILD) technique. The maximum dose of electrically active dopant atoms per laser pulse produced by the GILD technique is limited to a fraction of the adsorbed molecules (approximately 10<sup>13</sup> cm<sup>-2</sup>), with required dopant concentration typically being much higher (for example a minimum of 5 x 10<sup>14</sup> cm<sup>-2</sup>) for a



source/drain junction in FET type devices, while the dopant dose per pulse of the present invention is greater than  $10^{15}$  cm<sup>-2</sup>. Thus, while the GILD technique requires at least 50 doping pulses for practical use, the present invention requires only a few (1 to 10) pulses, thus greatly reducing the required number of pulses and improving the throughput of the doping process.

### BRIEF DESCRIPTION OF THE DRAWINGS

The accompanying drawings, which are incorporated into and form a part of the disclosure, illustrate an embodiment of the process of the invention and, together with the description, serve to explain the principles of the invention.

Figure 1 illustrates schematically the dopant deposition operation of the process carried out in accordance with the invention.

Figure 2 illustrates schematically the dopant incorporation following the pulsed laser or ion-beam processing carried out in accordance with the invention.

### DETAILED DESCRIPTION OF THE INVENTION

The present invention is directed to a doping process which enhances the dopant incorporation into a material, such as a semiconductor, achievable using doping techniques involving surface melting such as the gas immersion laser doping (GILD) technique. The doping process of this invention is a two-step process for incorporating, for example, an electrically active dopant into a semiconductor. However, the process can be utilized to effectively dope materials for other applications. Because the doping process

of this invention reduces the number of energy pulses required to achieve useful active dopant concentrations, compared to the number of energy pulses required using the GILD technique, the present invention will supersede and possibly replace the current GILD technique. As pointed out above, when using the GILD technique the maximum dose of electrically active dopant atoms per energy pulse is limited to a fraction of the adsorbed molecules (about  $10^{13} \text{ cm}^{-2}$ ). Useful dopant doses are above  $5 \times 10^{14} \text{ cm}^{-2}$ , and thus the GILD technique requires at least 50 doping pulses for practical use. Using the two-step process of this invention which enhances the dopant surface concentration followed with an energy pulse to incorporate the dopant into the semiconductor, the number of pulses is reduced to a few (1-10) to achieve useful active dopant concentrations. This enhances the manufacturability of pulsed laser and pulsed ion-beam semiconductor doping and junction formation by reducing the required number of energy pulses by a factor of 10.

The invention involves a combination of dopant deposition followed by pulsed laser or ion-beam dopant drive-in. By the process of this invention doping is enhanced by increasing the surface concentration (and possibly thickness) of dopant molecules on the semiconductor surface prior to the energy pulse, thus resulting in a greater dose of dopant per pulse. The first step is to deposit a layer of dopant molecules/atoms on the surface of the semiconductor. The second step is to use either a laser or ion-beam pulse to melt the surface region on the semiconductor. During the molten phase, the dopant molecules/atoms diffuse into the molten region of the semiconductor. Following solidification of the molten region, dopant atoms

are frozen into the semiconductor (i.e., incorporated onto lattice sites) and are electrically active.

The two-step doping process is described hereinafter with reference to Figures 1 and 2, wherein Figure 1 illustrates the first step of the process; namely, dopant deposition onto a semiconductor surface; and Figure 2 illustrates the second step of the process; namely, dopant incorporation into the semiconductor by pulsed laser or ion-beam processing. The two-step process increases surface concentration of dopant atoms/molecules compared to prior processes.

For the first step of the process (dopant deposition on a surface of a semiconductor), as seen in Figure 1, a variety of deposition techniques may be used, such as plasma enhanced chemical vapor deposition (PECVD) also known as glow discharge CVD, sputtering, condensation through cooling the semiconductor, thermal decomposition CVD (such as LPCVD or hot-wire CVD), and photolytic decomposition. In PECVD a dopant gas, such as  $\text{BF}_3$ ,  $\text{PF}_5$ ,  $\text{AsH}_3$ ,  $\text{B}_2\text{H}_6$ ,  $\text{AsF}_5$ , or  $\text{PH}_3$ , or an appropriate organometallic, is introduced into a glow discharge which decomposes the gas into chemicalactive radicals. These radicals attach to the semiconductor surface and form the dopant layer. By way of example, a glow discharge may be used to decompose  $\text{PF}_5$  to deposit a layer of phosphorus atoms/molecules 12 on the surface 13 of {100} silicon 11 of a semiconductor 10, as seen in Figure 1. This could be done by, for example, introducing 0.1 standard cubic centimeters of  $\text{PF}_5$  into a glow discharge of He operating at a pressure of 300 nTorr. With 30% conversion efficiency over an area of  $1000 \text{ cm}^2$ , this would result in a layer of phosphorus atoms 12 with a surface density of approximately  $8 \times 10^{14} \text{ atoms/cm}^2$ . This surface density is more than an order of magnitude greater

compared to that achieved by surface adsorption in the GILD technique, and can be easily increased by introducing more PF<sub>5</sub>. For sputtering, a standard sputtering technique is used to sputter a target containing the desired dopant to deposit a thin layer of the dopant. For example, magnetron sputtering is a well known technique as exemplified by U.S. Patents No. 5,203,977 and No. 5,232,571. Deposition by condensation is also a well known technique and is implemented by cooling the semiconductor in the presence of a dopant ambient (atmosphere), where the dopant layer is the condensant. Thermal decomposition CVD is a widely used technique for the desposition of thin films. For example, low pressure CVD (or LPCVD) is often used to thermally decompose SiH<sub>4</sub> or Si<sub>2</sub>H<sub>6</sub> gases to form thin films of silicon, and similar techniques could be used to form dopant layers using a dopant source gas (e.g., B<sub>2</sub>H<sub>6</sub>, AsH<sub>3</sub>, PF<sub>5</sub>, etc.). Photolytic decomposition is a CVD technique in which a dopant source gas is photolytically decomposed by an appropriate light source (e.g., UV lamp, excimer laser, etc.).

For the second step (dopant incorporation or dopant drive-in), the dopant layer of phosphorus atoms 12 shown in Figure 1 is incorporated or driven into, for example the semiconductor silicon 11, below the surface 13 upon exposure thereto of a pulse of laser or ion-beam energy or laser beam 14 from a source 15, such as, for example, an XeCl excimer laser, having a wavelength of 308 nm. The energy is absorbed in the silicon/dopant layer surface region and converted to thermal energy which melts an upper region of the silicon 11, as indicated at 16, and converts this region to crystalline polysilicon. During the time the silicon region 16 is molten, dopant atoms 12 diffuse rapidly into the silicon as a result of their higher diffusivity. Upon solidification of the silicon region 16, the dopant atoms are incorporated into

the polysilicon region 16, as indicated at 17, in the correct lattice positions to be electrically active sites.

By way of example, an XeCl excimer laser, operating at a wavelength of 308 nm, produces an energy pulse in the range of 50 to 1000 mJ cm<sup>-2</sup>, (millijoules per cm<sup>2</sup>) with a pulse length of 5 to 100 ns, whereby a dose of about  $8 \times 10^{14}$  cm<sup>-2</sup> electrically active dopant atoms per laser pulse is produced. The thickness or depth of the converted polysilicon region 16 is dependent on the energy density and length of the energy pulse(s). Other types of pulsed laser systems may be utilized to produce the desired pulse length and energy density, such may include other types of excimer lasers (e.g. XeF or KF), copper vapor lasers, dye lasers, and pulsed NdYAG lasers. Also, the pulsed energy density can be produced by existing pulsed ion-beam machines.

If the pulsed energy is produced by a laser system, such as exemplified above, a variety of laser wavelengths may be used so long as the wavelength is short enough, whereby the laser energy is absorbed in the near surface region of the silicon, for example. The pulsed duration and energy density can be varied to control the depth of melt, and in turn, the depth of dopant incorporation.

It would also be possible to use a pulsed ion beam instead of a pulsed laser beam to deliver energy to the near surface region of the material to be doped. Again, the pulse energy density and duration could be varied to control the depth of melt and doping.

The enhanced doping, made possible by the process of the present invention, will make possible the manufacture of: 1) active matrix flat panel displays on plastic substrates; and 2) shallow junction formation for

microelectronics on silicon, insulating or plastic substrates. Other uses include potential enabling technology for large area low cost electronics (such as flat panel displays), portable electronics, and ultra-submicron (deep submicron) semiconductor device fabrication. Also, by the use of this doping process, flexible displays are envisioned for use in field-deployable portable electronics, battlefield operations facilities, and the interiors of ships, tanks and aircraft. In addition, flexible detector arrays are envisioned for use in radiation (x-ray, gamma ray) detection. Also, shallow junction formation is critical for development use in future supercomputers.

It has thus been shown that the doping process of the present invention enhances the dopant incorporation achievable using the GILD technology, and a greater dose of electrically active dopant atoms per laser pulse is produced compared to the GILD technique. The doping process of this invention thus significantly reduces the number of doping pulses require to produce useful dopant doses of above  $5 \times 10^{14} \text{ cm}^{-2}$ , thereby enhancing the manufacturability of pulsed laser-based as well as pulsed ion-beam-based semiconductor doping and junction formation.

While a specific operational sequence, deposition techniques, pulsed energy sources, materials, parameters, etc., have been set forth to exemplify and describe the principles of the invention, such are not intended to be limiting. Modifications and changes may become apparent to those skilled in the art, and it is intended that the invention be limited only by the scope of the appended claims.

THE INVENTION CLAIMED IS

1. A doping process, consisting of:  
depositing dopant on a surface of a material to be doped; and  
incorporating the dopant into the material by pulsed energy  
processing.
2. The process of Claim 1, wherein depositing the dopant is carried  
out by a technique selected from the group consisting of plasma enhanced  
chemical vapor deposition, sputtering, condensation through cooling the  
material to be doped, thermal decomposition CVD, and photolytic  
decomposition.
3. The process of Claim 1, wherein depositing the dopant is carried  
out using a dopant atmospheric selected from the group consisting of  $\text{BF}_3$ ,  
 $\text{PF}_5$ ,  $\text{AsH}_3$ ,  $\text{B}_2\text{H}_6$ ,  $\text{PH}_3$ ,  $\text{AsF}_5$ , and organometallics.
4. The process of Claim 1, wherein the pulsed energy processing is  
carried out using pulsed energy selected from the group consisting of pulsed  
laser energy and pulsed ion-beam energy.



5. The process of Claim 4, wherein the pulsed laser energy is provided by a laser selected from the group consisting of excimer lasers, copper vapor lasers, dye lasers, and pulsed NdYAG lasers.

6. The process of Claim 5, wherein the pulsed laser energy is produced by an XeCl excimer laser.

7. The process of Claim 6, wherein the pulsed excimer laser is constructed to operate at a wavelength of 308 nm, and controlling the excimer laser to produce 1 to about 10 pulses with an energy pulse of 50 to 1000 mj cm<sup>-2</sup>, and with a pulse length of 5 to 100 ns.

8. The process of Claim 1, wherein the dopant depositing and the pulsed energy processing is carried out so as to produce a dose of 10<sup>14</sup> to 10<sup>15</sup> electrically active dopant atoms per energy pulse.

9. An improved semiconductor doping process, comprising:  
depositing a layer of dopant atoms/molecules on a surface of a semiconductor, followed by exposure to one or more energy pulses which melts a portion of the semiconductor, forming a molten region thereby causing the dopant atoms/molecules to be incorporated into the molten region; and

allowing the molten region to recrystallize whereby the dopant atoms/molecules are electrically active in the semiconductor.



10. The process of Claim 9, wherein depositing the layer of dopant atoms/molecules is carried out by a technique selected from the group consisting of PECVD, glow discharge CVD, sputtering, condensation, photolytic decomposition, and thermal decomposition CVD.

11. The process of Claim 10, wherein the depositing of the layer of dopant atoms/molecules is carried out in an atmosphere selected from the group consisting of  $\text{BF}_3$ ,  $\text{PF}_5$ ,  $\text{AsH}_3$ ,  $\text{B}_2\text{H}_6$ ,  $\text{AsF}_5$ ,  $\text{PH}_3$ , and organometallics.

12. The process of Claim 9, additionally including forming the one or more energy pulses using a pulsed ion-beam machine or a pulsed laser selected from the group consisting of excimer lasers, copper vapor lasers, dye lasers, and pulsed NdYAG lasers.

13. The process of Claim 9, wherein the one or more energy pulses is supplied with a wavelength such that the energy is absorbed in the near surface region of the semiconductor.

14. The process of Claim 9, wherein the one or more energy pulses have a pulse duration of less than 1 ms.

15. The process of Claim 9, wherein the one or more energy pulses are produced by an XeCl excimer laser.

16. The process of Claim 9, additionally including forming the semiconductor from at least a layer of silicon.

17. The process of Claim 16, wherein the molten region recrystallizes as doped polysilicon.

18. In a process for doping a semiconductor material using pulsed laser energy or pulsed ion-beam energy processing, the improvement comprising:

5 forming a layer of dopant atoms on a surface of the semiconductor material prior to pulsed energy processing.

19. The improvement of Claim 18, additionally including forming the dopant atoms from a material which is electrically active following the pulsed energy processing, and wherein forming of the layer of dopant atoms is carried out such that a dose of electrically active dopant atoms formed from  
5 a layer of dopant atoms per energy pulse is greater than a dose of dopant atoms formed by pulsed energy processing in the presence of a dopant atmosphere.

20. The improvement of Claim 18, wherein the pulsed energy processing is carried out using one or more pulses from an excimer laser or an ion beam machine.

ABSTRACT OF THE DISCLOSURE

A semiconductor doping process which enhances the dopant incorporation achievable using the Gas Immersion Laser Doping (GILD) technique. The enhanced doping is achieved by first depositing a thin layer of dopant atoms on a semiconductor surface followed by exposure to one or more pulses from either a laser or an ion-beam which melt a portion of the semiconductor to a desired depth, thus causing the dopant atoms to be incorporated into the molten region. After the molten region recrystallizes the dopant atoms are electrically active. The dopant atoms are deposited by plasma enhanced chemical vapor deposition (PECVD) or other known deposition techniques.

This enhanced doping will make possible the manufacture of:

- 1) active matrix flat panel displays on plastic substrates; 2) shallow junction formation for microelectronics on silicon, insulating or plastic substrates; and
- 3) junction formation for solar cells.

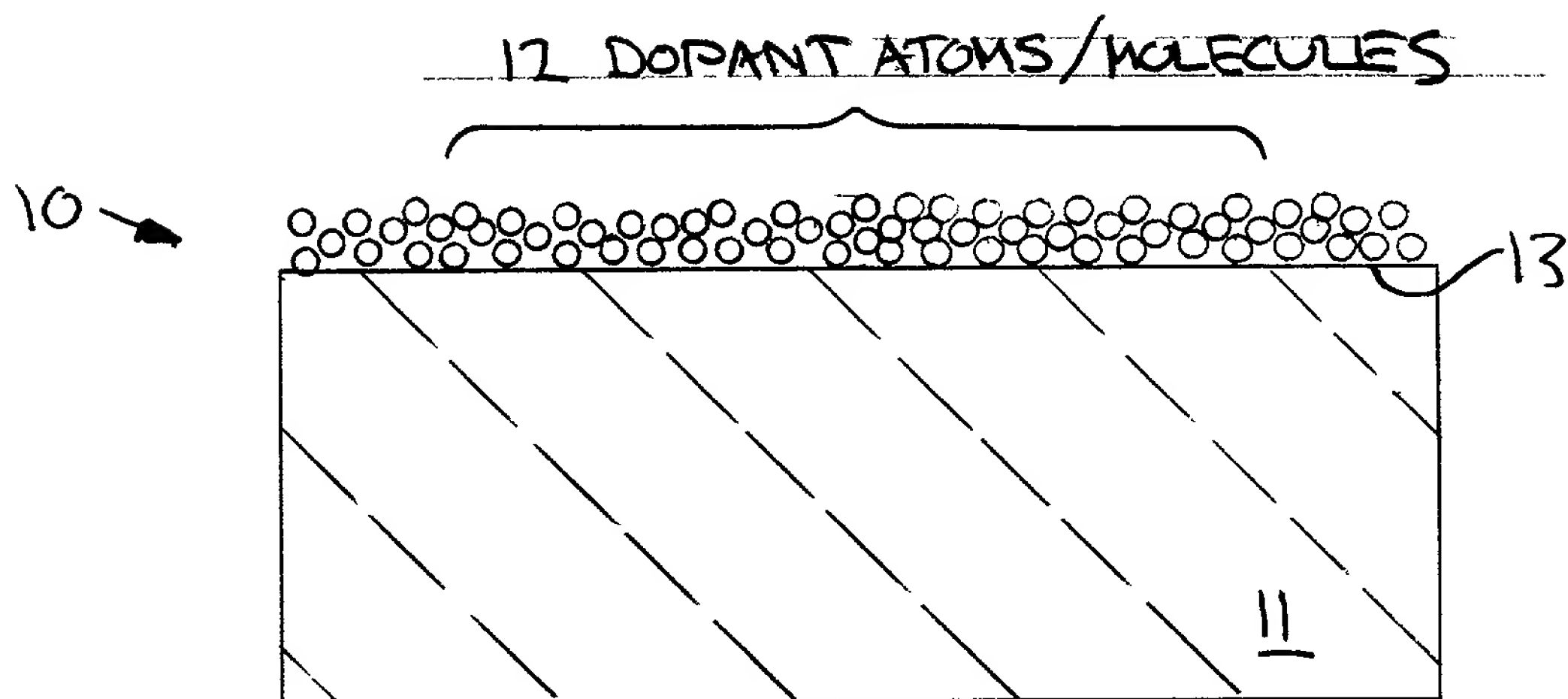


FIG. 1

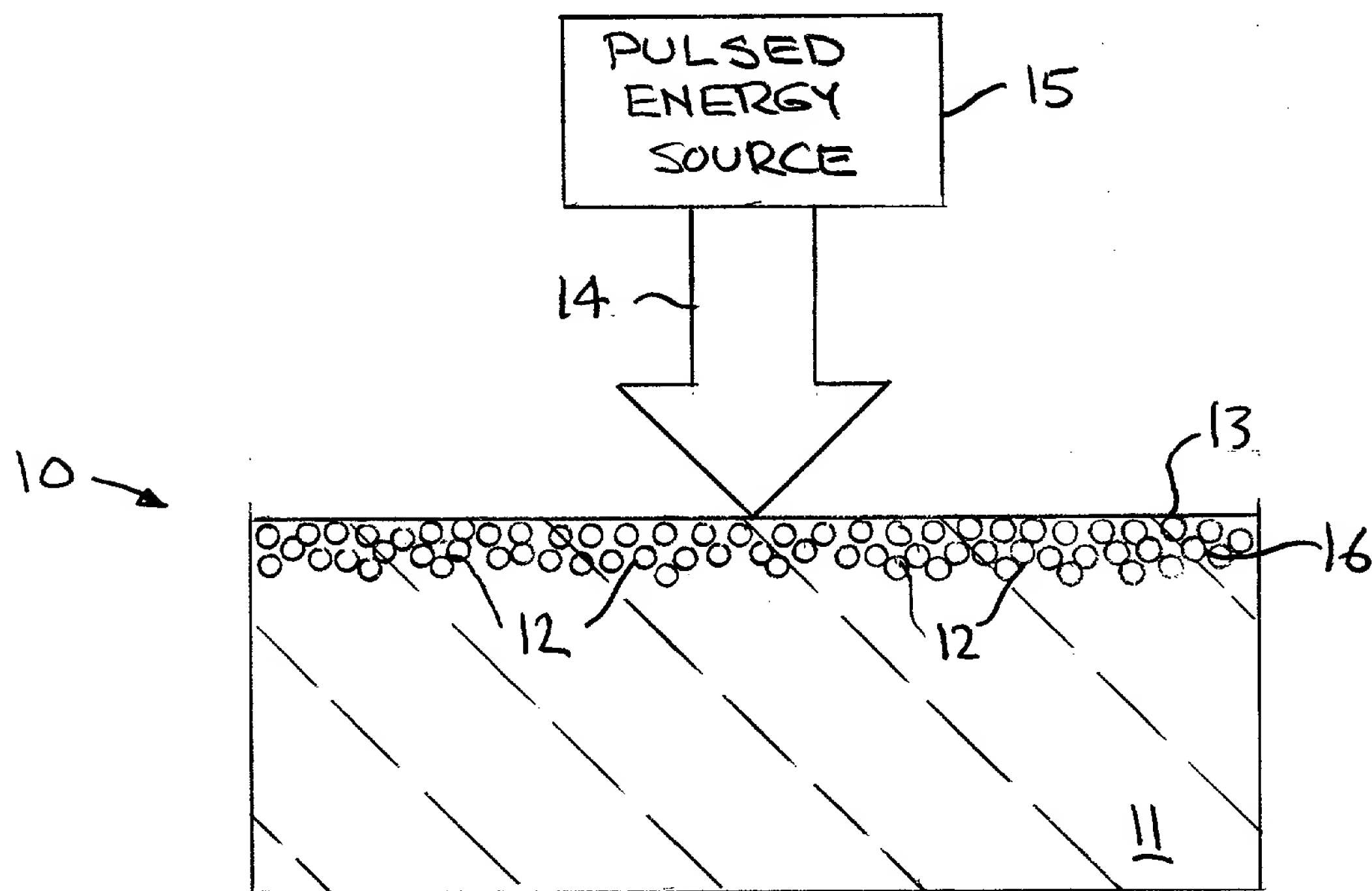


FIG. 2

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant : Paul Wickboldt et al.

Docket No. : IL-10092

Serial No. :

Art Unit :

Filed :

Batch No. :

For : Deposition Of Dopant  
Impurities And Pulsed  
Energy Drive-In

Examiner :

VERIFIED STATEMENT (DECLARATION) CLAIMING SMALL ENTITY  
STATUS [37 CFR 1.9 (f) and 1.27(d)] - NONPROFIT ORGANIZATION

I hereby declare that I am an official empowered to act on behalf of the nonprofit organization identified below:

The Regents of the University of California  
300 Lakeside Drive, 22nd Floor  
Oakland, CA 94612-3550

## TYPE OF ORGANIZATION

 X  University or Other Institution of Higher Education

I hereby declare that the nonprofit organization identified above qualifies as a nonprofit organization as defined in 37 CFR 1.9(e) for purposes of paying reduced fees under Section 41(a) and (b) of Title 35, United States Code with regard to the invention entitled Deposition Of Dopant Impurities And Pulsed Energy  
Drive-In

by inventor(s) Paul Wickboldt/Paul G. Carey/Patrick M. Smith/Albert R. Ellingboe

described in

 X  the specification filed herewith.  application serial no.  , filed  .  patent no.  , issued  .

I hereby declare that rights under contract or law have been conveyed to and remain with the nonprofit organization with regard to the above identified invention, except for a license to a Federal Agency pursuant to 35 USC 202(c) (4).

Each person, concern or organization to which I have assigned, granted, conveyed, or licensed or am under an obligation under contract or law to assign, grant, convey, or license any rights in the invention is listed below:

X no such person, concern, or organization

I acknowledge the duty to file, in this application or patent, notification of any change in status resulting in loss of entitlement to small entity status prior to paying, or at the time of paying, the earliest of the issue fee or any maintenance fee due after the date on which status as a small entity is no longer appropriate. (37 CFR 1.28(b))

I hereby declare that all statements made herein of my own knowledge are true and that all statements made on information and belief are believed to be true: and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under Section 1001 of Title 18 of the United States Code, and that such willful false statements may jeopardize the validity of the application, any patent issuing thereon, or any patent to which this verified statement is directed.

JANET G. TULK  
Laboratory Counsel  
Lawrence Livermore National Laboratory  
7000 East Avenue, L-701  
Livermore, CA 94551

Janet G. Tulk  
JANET G. TULK

5-29-97  
Date

## COMBINED DECLARATION AND POWER OF ATTORNEY

As the below named inventor(s), I (we) hereby declare that:

My (Our) residence, post office address and citizenship(s) are as stated below next to my (our) name(s).

I believe I am the original, first and sole inventor (if only one name is listed below) or an original, first and joint inventor (if plural names are listed below) of the subject matter which is claimed and for which a patent is sought on the invention entitled:\_\_\_\_\_

DEPOSITION OF DOPANT IMPURITIES AND PULSED ENERGY DRIVE-IN

the specification of which (check one)

X is attached hereto \_\_\_\_\_ was filed on \_\_\_\_\_ as Serial No. \_\_\_\_\_

and was amended on \_\_\_\_\_ (if applicable).

I (We) hereby state that I (we) have reviewed and understand the contents of the above-identified specification, including claims, as amended by any amendment referred to above.

I (We) acknowledge the duty to disclose information which is material to the examination of this application in accordance with Title 37, Code of Federal Regulations, § 1.56.

I (We) hereby claim the benefit under Title 35, United States Code, § 120 of any United States application(s) listed below and insofar as the subject matter of each of the claims of this application is not disclosed in the prior United States application in the manner provided by the first paragraph of Title 35, United States Code, § 112, I acknowledge the duty to disclose material information as defined in Title 37, Code of Federal Regulations, § 1.56 which occurred between the filing date of the prior application and the national or PCT international filing date of this application:

Serial No.	Filing Date	Status
	NONE	

<b>POWER OF ATTORNEY:</b> As the named inventor(s), I (we) hereby appoint the following attorney(s) and/or agent(s) to prosecute this application and transact all business in the Patent and Trademark office connected therewith.	
Names and Registration Nos.	
Henry P. Sartorio L.E. Carnahan	28,535 20,555
<u>Send Correspondence To:</u> Henry P. Sartorio Deputy Laboratory Counsel for Patents Lawrence Livermore National Laboratory P.O. Box 808 - L-703 Livermore, California 94551	<u>Direct Telephone Calls To:</u> (Name and Telephone Numbers)  Henry P. Sartorio (510) 422-7816

We hereby declare that all statements made herein of our own knowledge are true and that all statements made on information and belief are believed to be true; and further that these statements were made with the knowledge that willful false statements and the like so made are punishable by fine or imprisonment, or both, under § 1001 of Title 18 of the United States Code and that such willful false statements may jeopardize the validity of the application or any patent issuing thereon.

PAUL WICKBOLDT	<i>Paul Wickboldt</i>
Full Name of Inventor	Signature
WALNUT CREEK, CALIFORNIA	5/30/97
Residence (City, State or Foreign Country)	Date
1164 LINCOLN AVENUE #146, WALNUT CREEK, CA 94596	USA
Postal Address (Street, City, State, Zip Code)	Citizenship
XX	
PAUL G. CAREY	<i>Paul G. Carey</i>
Full Name of Inventor	Signature
MOUNTAIN VIEW, CALIFORNIA	5-30-97
Residence (City, State or Foreign Country)	Date
373 PAUL AVENUE, MOUNTAIN VIEW, CA 94041	USA
Postal Address (Street, City, State, Zip Code)	Citizenship
XX	
PATRICK M. SMITH	<i>Patrick M. Smith</i>
Full Name of Inventor	Signature
SAN RAMON, CALIFORNIA	5/30/97
Residence (City, State or Foreign Country)	Date
555 CANYON WOOD CIRCLE #250, SAN RAMON, CA 94583	USA
Postal Address (Street, City, State, Zip Code)	Citizenship
XX	
ALBERT R. ELLINGBOE	<i>Albert R. Ellingboe</i>
Full Name of Inventor	Signature
FREMONT, CALIFORNIA	30 May 1997
Residence (City, State or Foreign Country)	Date
426 WASHINGTON BOULEVARD, FREMONT, CA 94539	USA
Postal Address (Street, City, State, Zip Code)	Citizenship
XX	
Full Name of Inventor	Signature
Residence (City, State or Foreign Country)	Date
Postal Address (Street, City, State, Zip Code)	Citizenship